Pramod Murali

Contact Information

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Education

University of California Berkeley, CA, USA

Ph.D. Candidate, EE, August, 2012 - present

- Advisor: Prof. Bernhard Boser
- GPA: 3.95/4.0

Indian Institute of Science , Bangalore, India

M.E., Microelectronic Systems (ECE), August, 2009 - July, 2011

- Thesis Topic: "A Gas Sensor Array Platform in 0.35 μm CMOS Technology"
- Advisor: Prof. Navakanta Bhat
- GPA: 7.7/8 (Highest in class)

National Institute of Technology Karnataka, Surathkal, India

B.E., ECE, August, 2002 - July, 2006

- Project Topic: "Widely Programmable Active-RC Filter at 0.5 V Supply in 0.18 μm CMOS"
- Advisor: Prof. Tonse Lakshminidhi
- GPA: 9.31/10 (Second in class)

Research Interests

CMOS Sensor Interface Circuits, Radio Frequency ICs, Biosensors.

Software Skills

MATLAB, Cadence, VHDL, LabView, HFSS.

Hardware Skills

Equipments used for CMOS, Microfluidic and MEMS Fabrication and Characterization such as Sputtering, Evaporation, Lithography, SEM, VNA, FIB, Wirebonding etc.

Professional Experience

Project Associate, Centre for NanoScience and Engineering Aug, 2009 - Jul, 2012
Developed a Gas Sensor System for volatile organic gas detection and classification. Worked on developing an automated pico-liter dispenser.

Honors and Awards

The Alumni Award 2012, Indian Institute of Science, Bangalore.

Research Visitor, Biomedical Diagnostics Institute, Dublin City University, Dublin. August - September 2011.

Secured an All India Rank 4 (Percentile: 99.99) in GATE 2009 (Graduate Aptitude Test for Engineering) in the Electronics and Communication stream.

Publications

[1] M. Pramod, N. Bhat, G. Banerjee, B. Amrutur, K. Bhat, and P. C. Ramamurthy, "CMOS Gas Sensor Array Platform with Fourier Transform Based Impedance Spectroscopy," in *VLSI Design (VLSID), 2012 25th International Conference on.* IEEE, 2012, pp. 173–178.

[2] P. Murali, N. Bhat, G. Banerjee, B. Amrutur, K. Bhat, and P. Ramamurthy, "A CMOS Gas Sensor Array Platform With Fourier Transform Based Impedance Spectroscopy," *Transactions on Circuits and Systems I*, vol. 59, no. 11, pp. 2507–2517, 2012.

[3] G. R. Prashanth, P. Murali, M. M. Varma, and N. Bhat, "In-Situ Impedance Spectroscopy of Layer-by-Layer Self-Assembly of Polyelectrolytes," in *International Conference on Emerging Electronics*, 2012.

[4] M. Pramod and K. Vinoy, "Electronically Tunable Zero Order Resonator Based on CRLH-TLs," in *Communications (NCC), 2011 National Conference on.* IEEE, 2011, pp. 1–3.

[5] M. Pramod, "Energy Efficient Integrated Gas Sensor System with Post CMOS Functionalization," in *Integrated Circuits (ISIC), 2011 13th International Symposium on.* IEEE, 2011, pp. 130–135.

[6] M. Pramod and T. Laxminidhi, "Low Power Continuous Time Common Mode Sensing for Common Mode Feedback Circuits," *Journal of Circuits, Systems, and Computers*, vol. 19, no. 3, pp. 519–528, 2010.

[7] M. Pramod and K. Vinoy, "Electronically Tunable Band Pass Filter Based On CRLH-TLs," in *International Symposium on Microwaves*, 2010.

[8] K. Soorya Krishna, M. Pramod, and M. Bhat, "Estimation of Interconnect Metrics using State Space Approach," in *Industrial and Information Systems (ICIIS), 2010 International Conference on.* IEEE, 2010, pp. 255–260.

[9] K. Soorya Krishna, M. Pramod, and M. S. Bhat, "Modelling of Single, Coupled, L and T Type Interconnects using State Space Approach," *International Journal of Signal and Imaging Systems Engineering*, vol. 2, no. 4, pp. 216–223, 2009.