

C. T.-C. Nguyen, "Communication architectures based on high- $Q$  MEMS devices (invited)," *Workshop Notes*, Workshop on Microwave and Photonic Applications of MEMS at the 2000 IEEE MTT-S International Microwave Symposium, Anaheim, California, June 16, 2000.

## ***Communication Architectures Based on High- $Q$ MEMS Devices***

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The increasing demand for mobile wireless communications has stimulated interest in compact, low power, inexpensive transceivers. This, along with the current trend to implement complete systems on single silicon chips, makes desirable completely monolithic versions of such transceivers. To date, many of the proposed strategies for achieving single-chip transceivers have evolved from the premise that the off-chip high- $Q$  RF and IF filters and oscillators used in heterodyning transceiver architectures cannot be miniaturized. Specifically, tank circuits with  $Q$ 's greater than 100 have not been achievable using conventional planar IC technologies in the frequency ranges of interest.

The rapid growth of IC-compatible micromachining technologies that yield micro-scale, high- $Q$  tank components may now allow miniaturized, low-power transceivers based upon traditional super-heterodyne architectures. Specifically, the high- $Q$  RF and IF filters and low phase noise oscillators currently implemented via off-chip resonators and discrete passives may now potentially be realized on the micro-scale using micromachined vibrating mechanical resonators with  $Q$ 's in the thousands, orders of magnitude smaller size, and the ability to be integrated alongside IC transistors. Once these miniaturized filters and oscillators become available, the fundamental bases upon which communication systems are developed may also evolve, giving rise to new system architectures with possible power and bandwidth efficiency advantages.

As an example, one of the more revolutionary potential uses of micromechanical resonator devices takes advantage of their tiny size and zero dc power consumption. Such features make possible the use of hundreds, perhaps thousands of them, to form banks of interlinked, on/off switchable micromechanical filters that can potentially serve as an RF channel-select filter bank—something presently unattainable with today's macroscopic technologies. With RF channel-selection, adjacent channel interferers can be removed *before* they reach subsequent RF transistor electronics, allowing substantial reductions in the dynamic range and phase noise specifications in those electronics, leading in turn to substantial power savings. In effect, a paradigm-shift in receiver architectural design may be possible, where instead of minimizing the number of high- $Q$  components in a given system, the use of such components is maximized in an attempt to harness the  $Q$  vs. power trade-off commonly seen in communications design. Cost reduction is also possible, since the above performance relaxations may also allow the realization of certain transceiver stages in less expensive transistor technologies (e.g., all silicon?).

In a broader sense, these ideas can be taken a step further by recognizing that the subject mechanical resonators are actually mechanical links that can be thought of as tiny circuit elements, much like resistors or individual transistors. Like a single transistor, a single mechanical link does not possess adequate processing power for most applications. However, again like transistors, when hooked up into larger (potentially, VLSI) circuits, the true power of micromechanical links can be unleashed, and signal processing functions previously inaccessible to transistor circuits may become feasible. (High- $Q$  micromechanical filters, comprised of interlinked resonators, represent one simple example of this. Mixer-filter devices are yet another.)

This talk explores the above possibilities, first giving an overview of the micromechanical circuits useful for communications applications, then suggesting potential receiver architectures that utilize MEMS technology to greatly improve communication sub-system performance.

# **Communication Architectures Based on High-Q MEMS Devices**

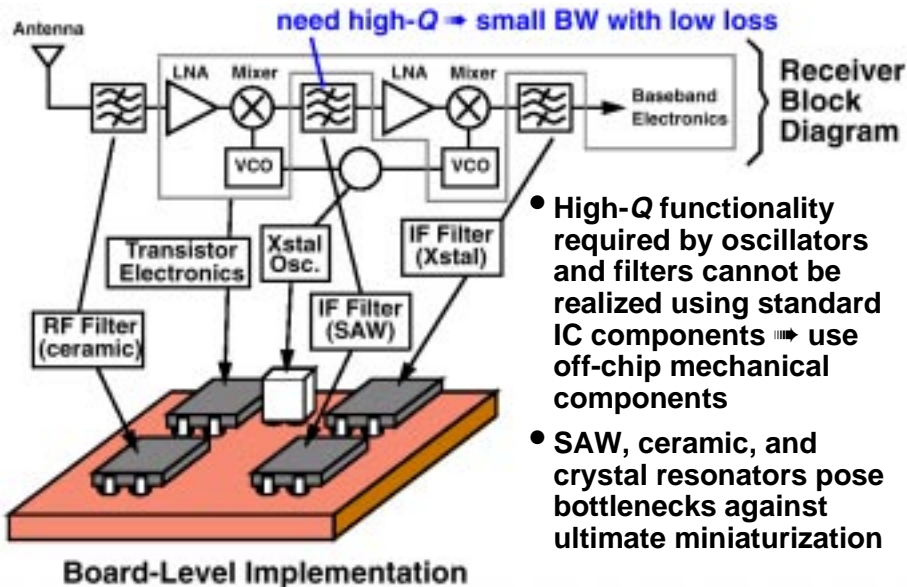
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## **Outline**

- **Miniaturization of Transceivers**
- **High-Q Micromechanical Resonators**
- **Micromechanical Circuits**
  - ↗ **micromechanical filters**
  - ↗ **micromechanical mixer-filters**
  - ↗ **micromechanical switches**
- **Power Savings Via High-Q MEMS**
  - ↗ **trade  $Q$  (or selectivity) for power**
- **MEMS-Based Transceiver Architecture**
- **Conclusions**

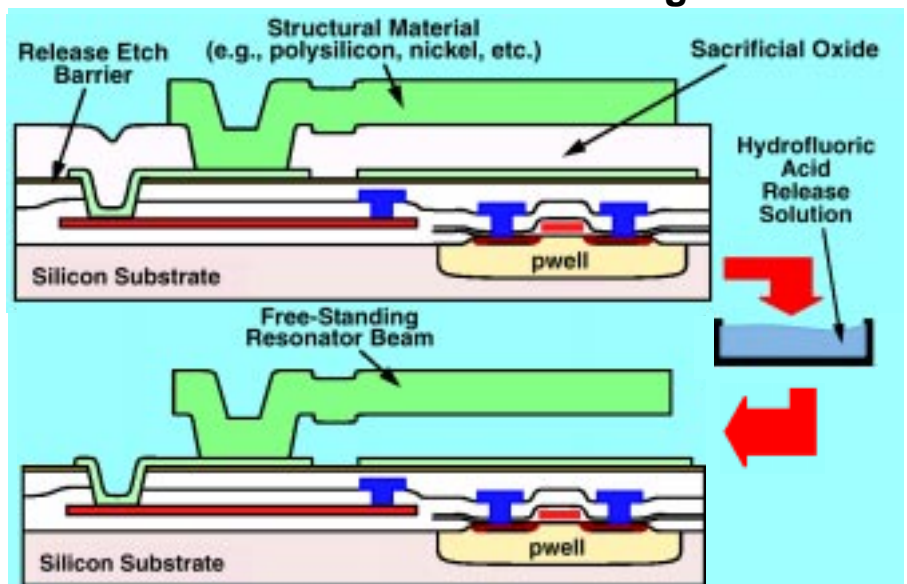
### Miniaturization of Transceivers



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### Surface Micromachining

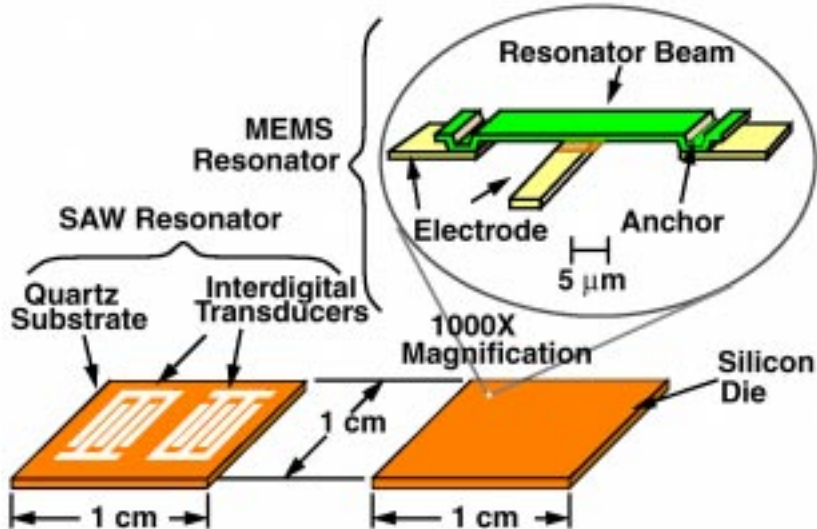


- Fabrication steps compatible with planar IC processing

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### MEMS vs. SAW Comparison

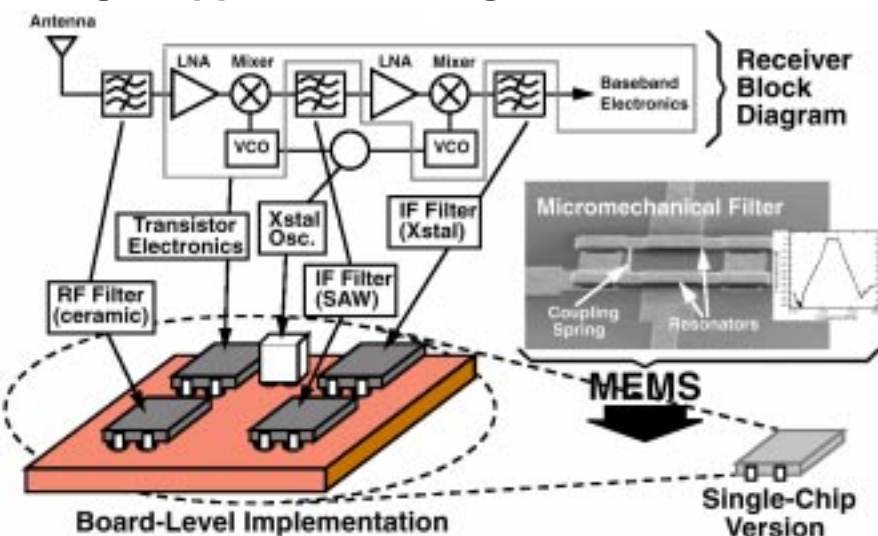


- MEMS offers the same or better high-Q frequency selectivity with orders of magnitude smaller size

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### Target Application: Integrated Transceivers



- Off-chip high-Q mechanical components present bottlenecks to miniaturization  $\Rightarrow$  replace them with  $\mu$ mechanical versions

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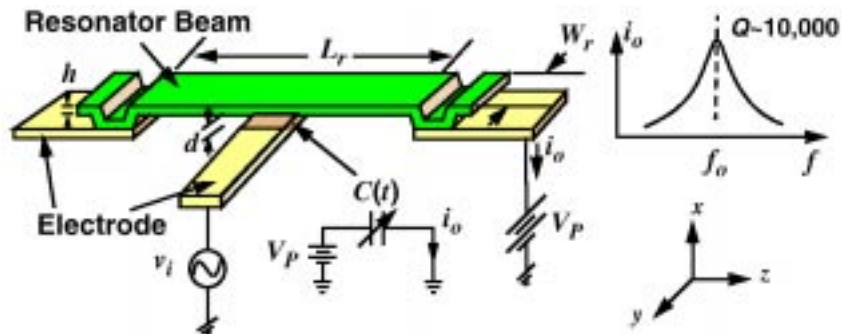
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## Outline

- Miniaturization of Transceivers
  - ☞ • High-Q Micromechanical Resonators
  - ☞ • Micromechanical Circuits
    - ☞ micromechanical filters
    - ☞ micromechanical mixer-filters
    - ☞ micromechanical switches
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## Vertically-Driven Micromechanical Resonator

- To date, most used design to achieve VHF frequencies



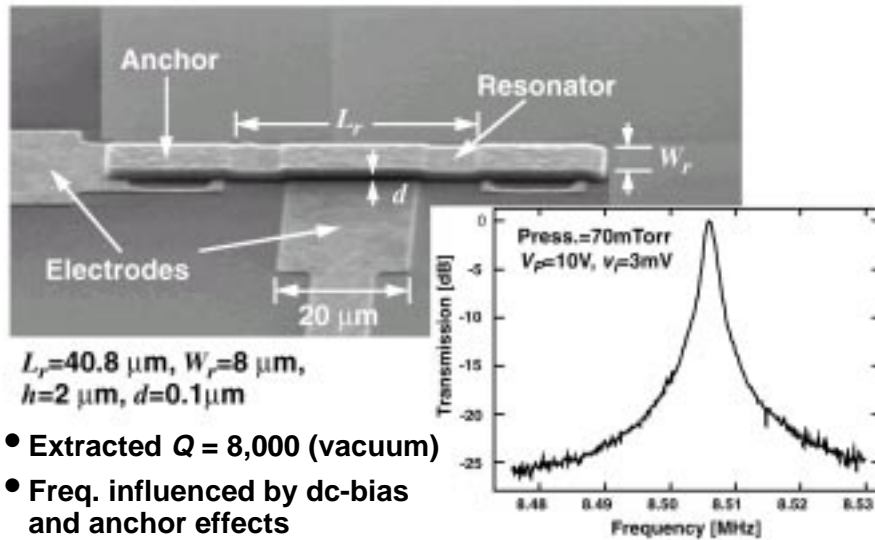
$$f_o = \frac{1}{2\pi} \sqrt{\frac{k_r}{m_r}} = 1.03 \sqrt{\frac{E}{\rho}} \frac{h}{L_r^2}$$

$E = \text{Youngs Modulus}$   
 $\rho = \text{density}$   
 (e.g.  $m_r = 10^{-13} \text{ kg}$ )

- Smaller mass  $\Rightarrow$  higher frequency range and lower series  $R_x$

### Fabricated HF $\mu$ Mechanical Resonator

- Surface-micromachined,  $\text{POCl}_3$ -doped polycrystalline silicon



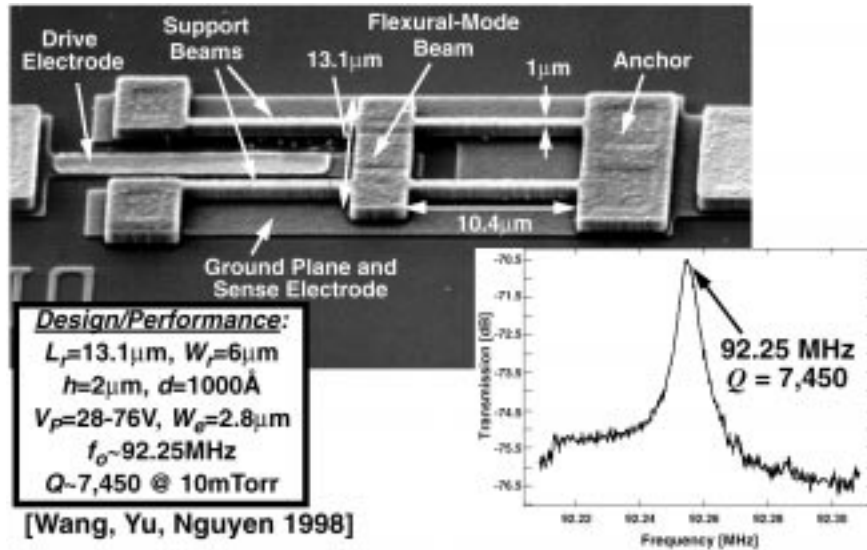
- Extracted  $Q = 8,000$  (vacuum)
- Freq. influenced by dc-bias and anchor effects

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### 92 MHz Free-Free Beam $\mu$ Resonator

- Free-free beam  $\mu$ mechanical resonator with non-intrusive supports  $\Rightarrow$  reduce anchor dissipation  $\Rightarrow$  higher  $Q$

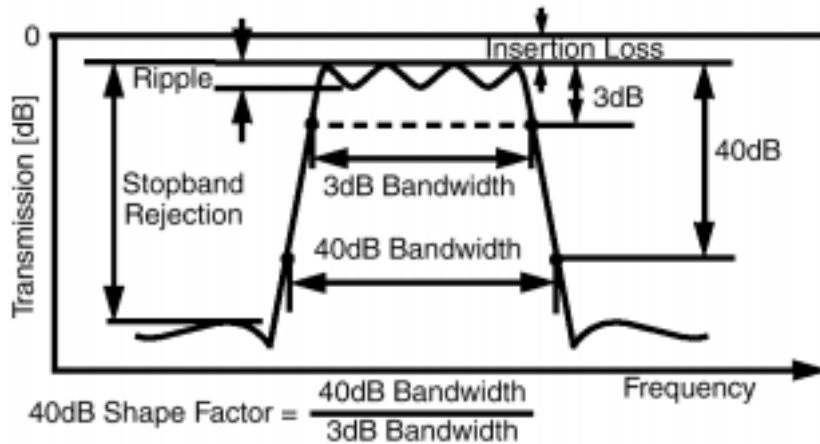


[Wang, Yu, Nguyen 1998]

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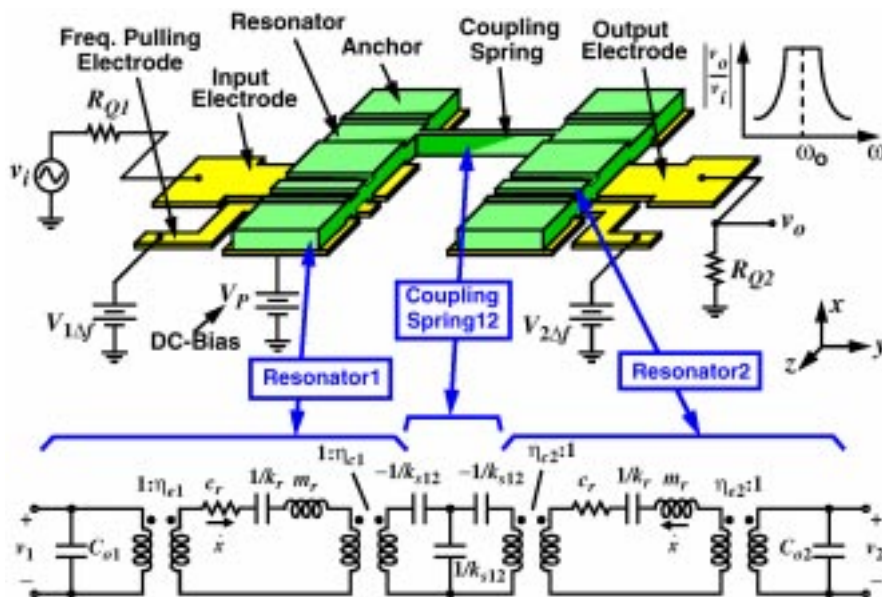
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### Desired Filter Characteristics



- Small shape factor generally preferred

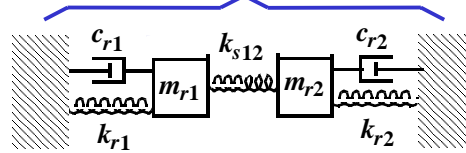
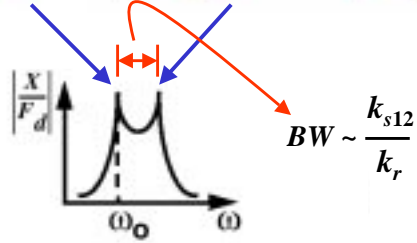
### Micromechanical Filter Circuit



### Ideal Spring-Coupled $\mu$ Mechanical Filter

Symmetric Mode

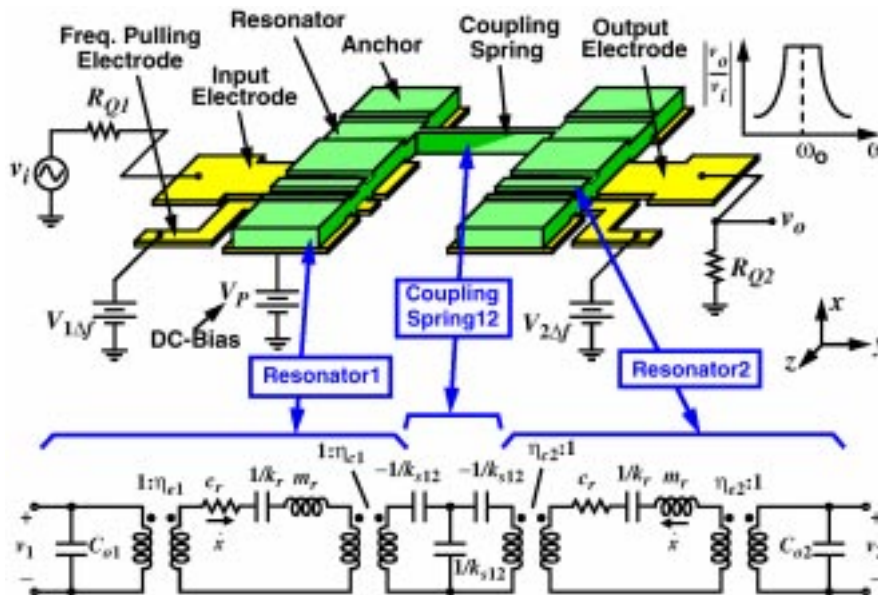
Anti-Symmetric Mode



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### Micromechanical Filter Circuit

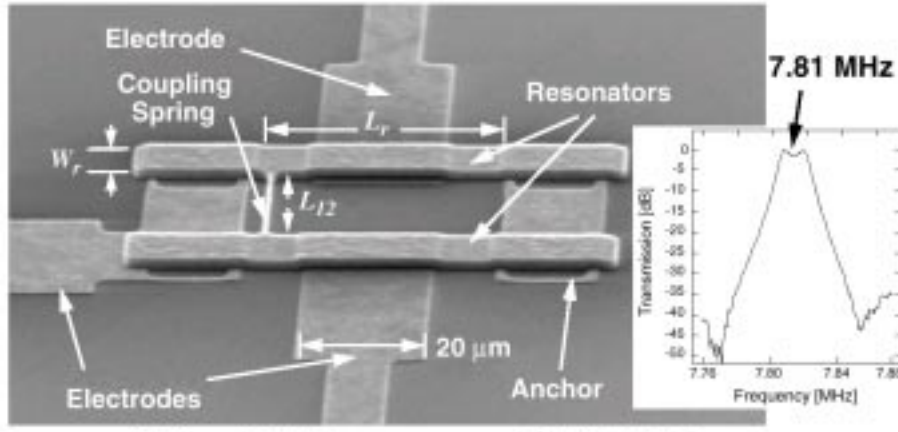


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### HF Spring-Coupled Micromechanical Filter



**2-Resonator HF**  
(4th Order)  
[Bannon, Clark,  
Nguyen 1996]

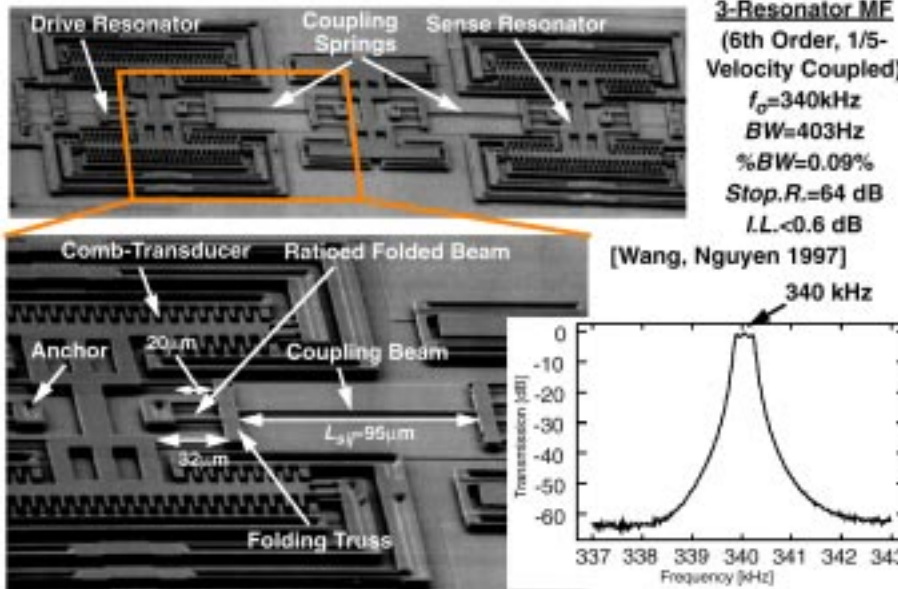


**Performance**  
 $f_o=7.81\text{MHz}$ ,  $BW=15\text{kHz}$   
 $\text{Rej.}=35\text{dB}$ ,  $\text{I.L.}<2\text{dB}$

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### High-Order $\mu$ Mechanical Filter



**3-Resonator MF**  
(6th Order, 1/5-  
Velocity Coupled)  
 $f_o=340\text{kHz}$   
 $BW=403\text{Hz}$   
 $\%BW=0.09\%$   
 $\text{Stop.R.}=64 \text{ dB}$   
 $\text{I.L.}<0.6 \text{ dB}$

[Wang, Nguyen 1997]

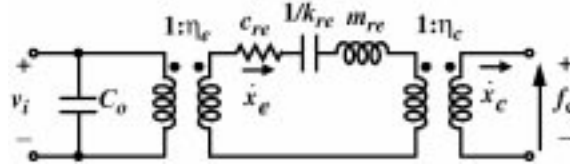
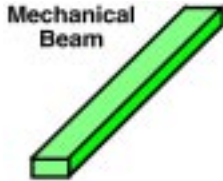
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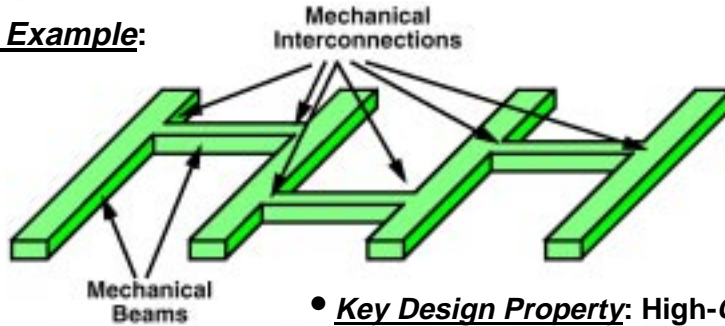
### Micromechanical Circuits

**Fundamental Building Block:**

**Equivalent Building Block Ckt.:**

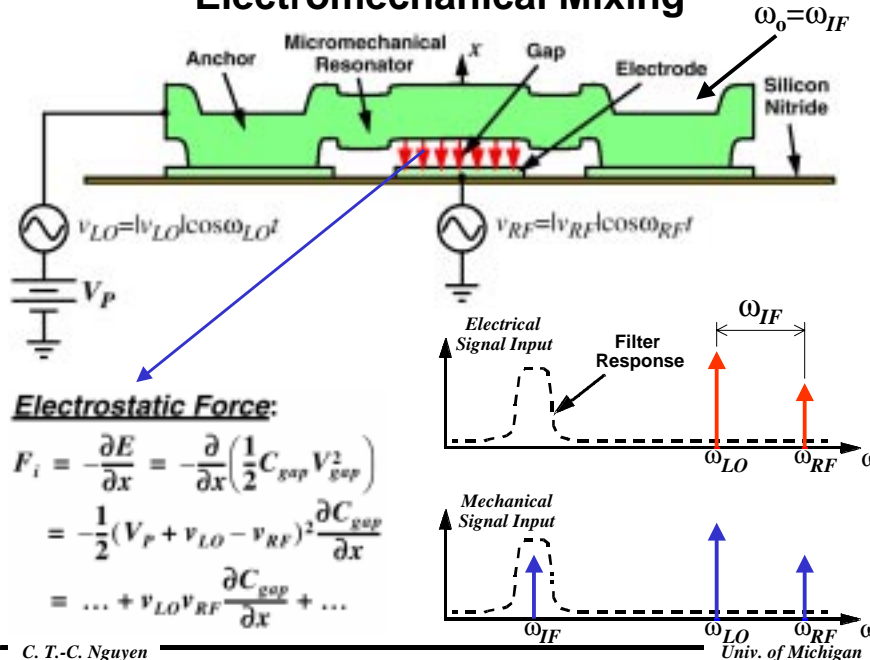


**Circuit Example:**

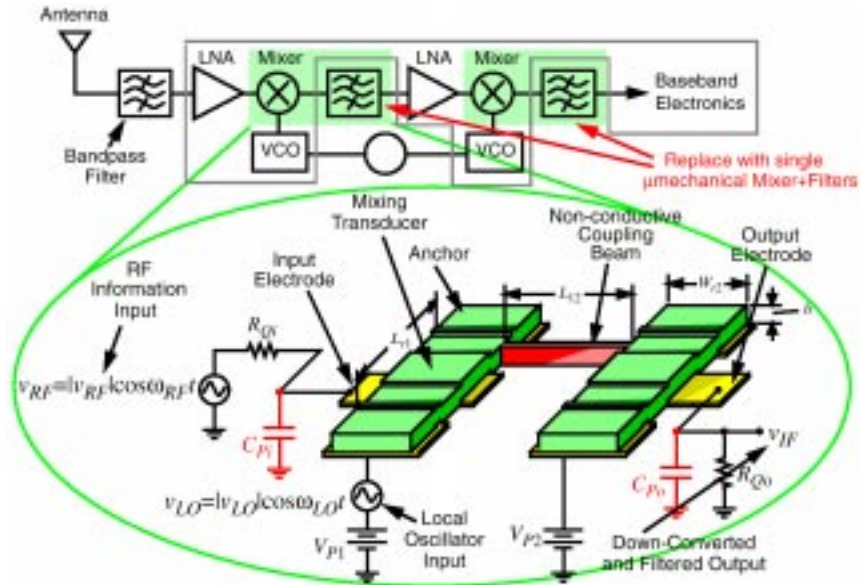


• **Key Design Property: High-Q**

### Electromechanical Mixing



## Micromechanical Mixer-Filter

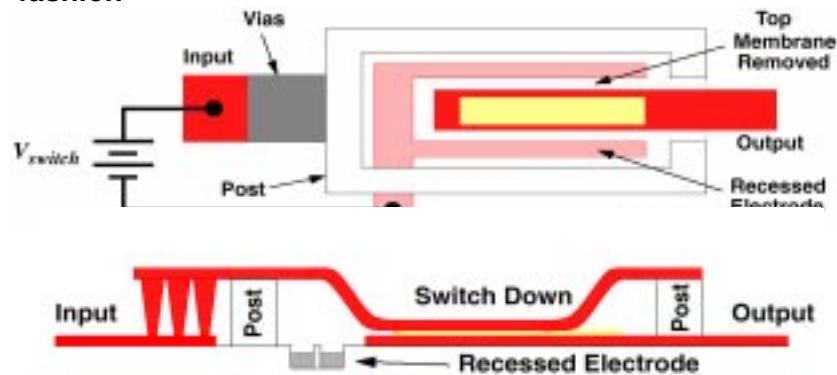


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## Micromechanical Switch

- Operate the micromechanical beam in an up/down binary fashion



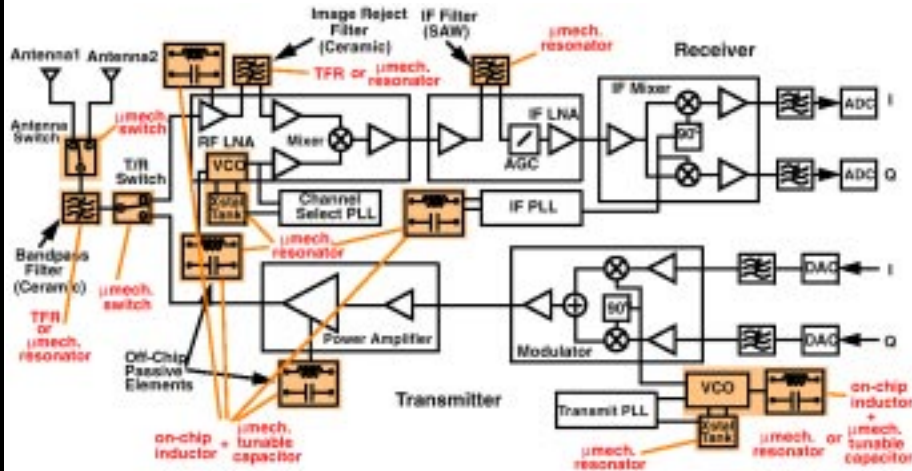
[C. Goldsmith, 1995]

- **Performance:**  $I.L. \sim 0.1\text{dB}$ ,  $IIP3 \sim 66\text{dBm}$  (extremely linear)
- **Issues:** switching voltage  $\sim 20\text{V}$ , switching time:  $10\text{-}100\mu\text{s}$

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## MEMS-Replaceable Transceiver Components



- A large number of off-chip high-Q components replaceable with  $\mu$ machined versions; e.g., using  $\mu$ machined resonators, switches, capacitors, and inductors

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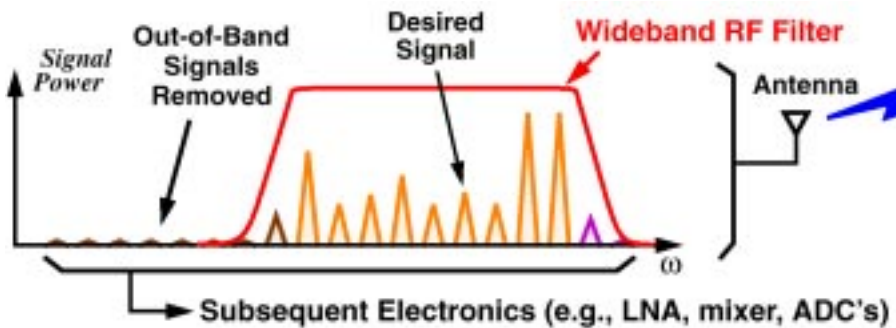
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## Power Versus Selectivity (or Q) Trade-Offs

- **Example:** power consumption as a function of front-end selectivity

↳ **case:** wideband front-end filtering

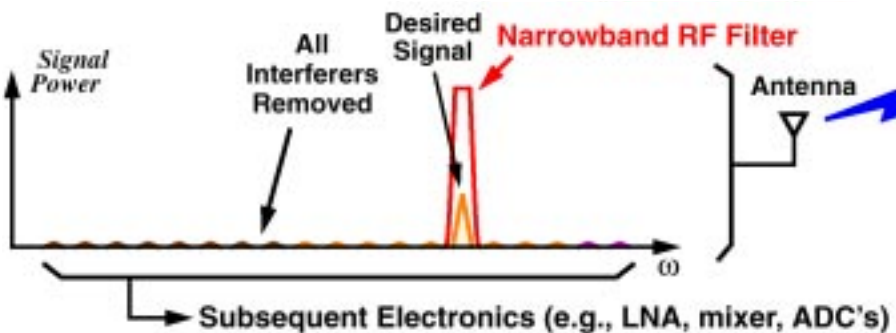


- **Problem:** subsequent electronics must have sufficient dynamic range to handle all in-band signals → must consume power to attain the needed dynamic range

## Power Versus Selectivity (or Q) Trade-Offs

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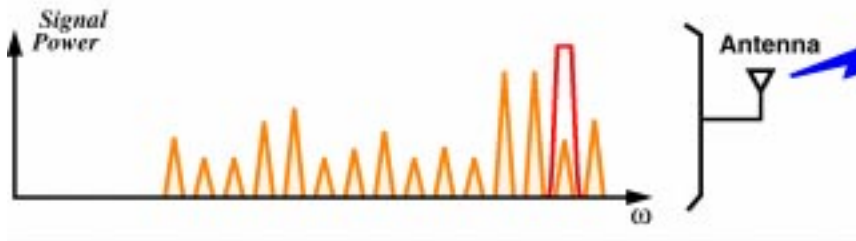
↳ **case:** narrowband front-end filtering



- **Result:** substantial power savings in subsequent circuits
  - ↳ relaxed dynamic range requirements
  - ↳ relaxed oscillator phase noise requirements

## Front-End Channel Selection

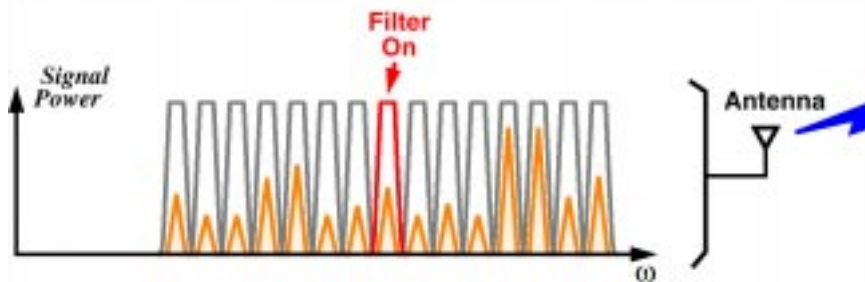
- **Power Saving Strategy:** select channels right up at RF
- **Approach:** Use a highly selective low-loss filter that is tunable from channel to channel:



- **Problem:** high filter selectivity (i.e., high  $Q$ ) often precludes tunability

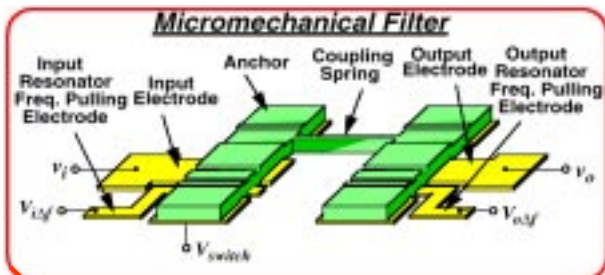
## Parallel Bank of Switchable Filters

- Rather than cover the band by tuning, cover with a bank of switchable filters

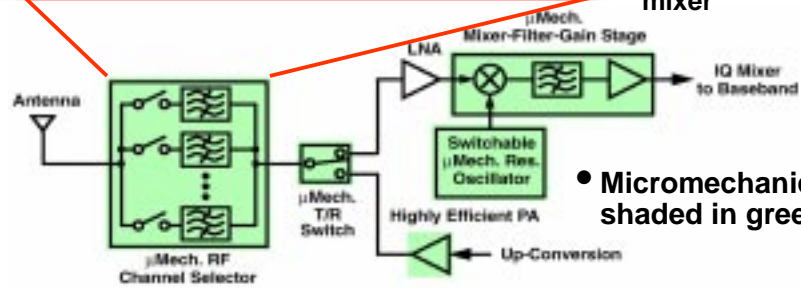


- **Problem:** macroscopic high- $Q$  filters are too big
- **Requirement:** tiny filters  $\implies$  micromechanical high- $Q$  filters present a good solution

## MEMS-Based Transceiver Architecture



- Use numerous filters in a switchable bank to allow front-end channel selection
- Allows more efficient PA and lower dynamic range LNA and mixer

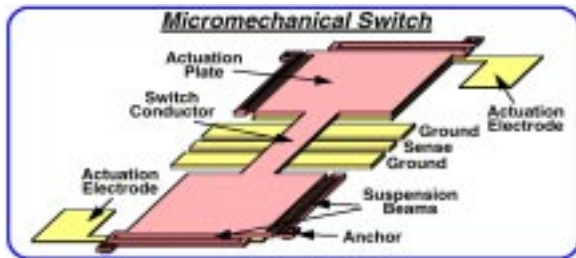


- Micromechanics are shaded in green

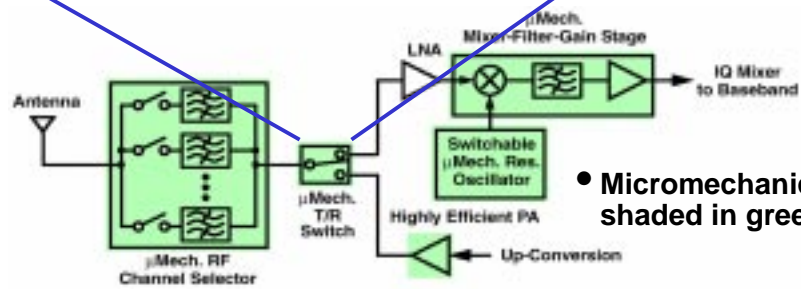
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## MEMS-Based Transceiver Architecture



- When replace FET switch: I.L. goes from 2dB to 0.1dB
- Save 280mW when transmitting 500mW

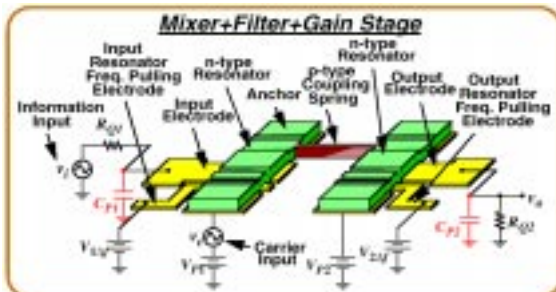


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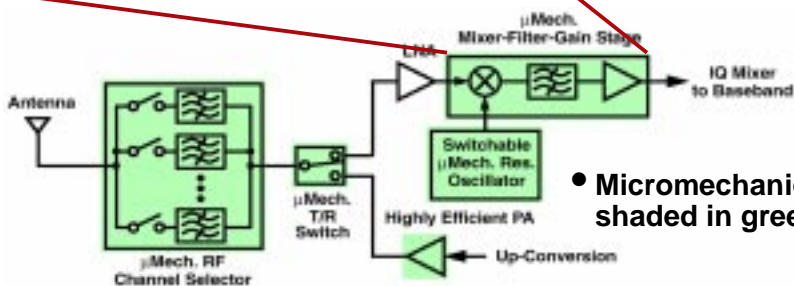
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## MEMS-Based Transceiver Architecture



- Use transducer nonlinearity to obtain a mixer function, followed by a filter
- Eliminate active mixer power

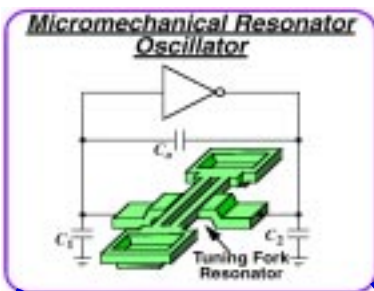


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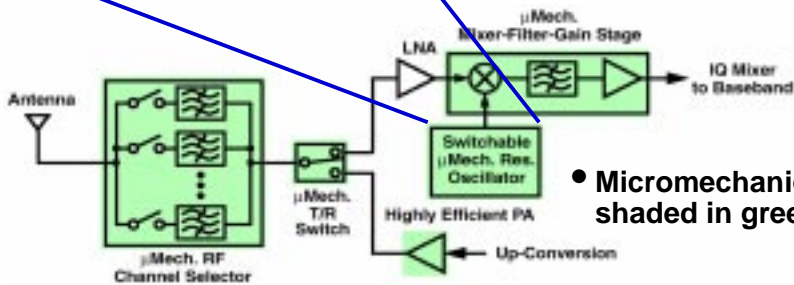
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## MEMS-Based Transceiver Architecture



- Substantial power savings if resonator  $Q > 1,000$
- Another example of  $Q$  versus power trade-off



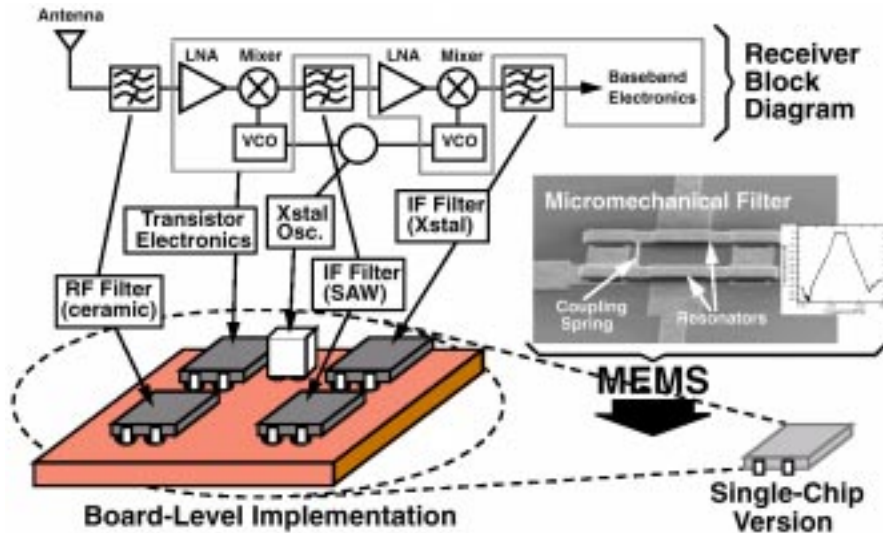
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## **Target Application: Integrated Transceivers**



- Off-chip high-Q mechanical components present bottlenecks to miniaturization  $\Rightarrow$  replace them with  $\mu$ mechanical versions

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## **Conclusions:**

- Via enhanced selectivity on a massive scale, micromechanical circuits using high-Q elements have the potential for shifting communication transceiver design paradigms, greatly enhancing their capabilities
- **Advantages of Micromechanical Circuits:**
  - $\Leftarrow$  orders of magnitude smaller size than present mechanical resonator devices
  - $\Leftarrow$  better performance than other single-chip solutions
  - $\Leftarrow$  potentially large reduction in power consumption
  - $\Leftarrow$  alternative transceiver architectures that maximize the use of high-Q, frequency selective devices for improved performance

**... but there is much work yet to be done ...**

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## **Acknowledgments**

- **Former and present graduate students, especially Kun Wang, Frank Bannon III, and Ark-Chew Wong, who are largely responsible for the micromechanical filter work, and Wan-Thai Hsu, Michael McCorquodale, and Mustafa Demirci, who are largely responsible for the resonator work**
- **My government funding sources: DARPA, NASA/JPL, NSF, and an ARO MURI**