

# Bryan August Brady

1141 Colusa Ave. • Berkeley, CA, 94707 • 412-551-1625 • bryan.brady@gmail.com

## EDUCATION:

*University of California, Berkeley, College of Engineering* – Berkeley, CA  
Ph.D. in Electrical Engineering (in progress)

*University of Pittsburgh, School of Engineering* – Pittsburgh, PA  
Master of Science in Electrical Engineering, 2005  
System-on-a-Chip Certificate

*University of Pittsburgh, School of Engineering* – Pittsburgh, PA  
Bachelor of Science in Computer Engineering, 2003  
Dean's List every semester

## EXPERIENCE:

**UC Berkeley – Graduate Student Researcher, Professor Seshia's Group** 01/06-present

- Developing automatic abstraction techniques for Verilog
- Developing and evaluating decision procedures and effects of various circuit encodings
- Modeling systems in UCLID such as pipelined processors and discretized hybrid automata

**UC Berkeley, University of Pittsburgh – Graduate Student Instructor/Teaching Assistant** Every semester during graduate study until 2006

- EECS 150: Components and Design Techniques for Digital Systems (UCB)
- ECE 142: Computer Organization and Design (Pitt)
- ECE 501: Digital Systems Laboratory (Pitt)
- ECE 1186: Software Engineering (Pitt)
- ECE 1192: Introduction to VLSI Design (Pitt)
- ECE 1193: Advanced VLSI Design (Pitt)
- ECE 1238: Digital Electronics (Pitt)

**ANSYS, Inc - Canonsburg, PA** 05/02-08/02

- Geometry Modeling Group: Planned and designed new features, implemented geometry modeling and manipulation features, optimized existing code 09/01-12/01
- Integrated Testing Group: Created regression tests, created test plans, verified bug fixes and program functionality 01/01-04/01

## PUBLICATIONS:

- **Selective Term-Level Abstraction of RTL Designs.** B. A. Brady, R. E. Bryant, S. A. Seshia. In submission
- **Deciding Bit-Vector Arithmetic with Abstraction.** R. E. Bryant, D. Kroening, J. Ouaknine, S. A. Seshia, O. Strichman, B. A. Brady, TACAS, March 2007.
- **Symbolic Reachability Analysis of Lazy Linear Hybrid Automata.** S. K. Jha, B. A. Brady, S. A. Seshia. In the 5<sup>th</sup> Intl. Conf. on Formal Modeling and Analysis of Timed Systems (FORMATS), October 2007, pages 241-256
- **Efficient CAD Development for Emerging Technologies Using Objective-C and Cocoa.** B. A. Brady, A. K. Jones, I. S. Kourtev. ICECS 2004

## COMPUTER SKILLS:

- C, C++, Perl, Java, JavaScript, Visual Basic, Verilog, VHDL, Assembly
- MS Visual Studio, GCC, GDB, UCLID
- Linux, UNIX, Mac OS X, Windows, MS Office

## ADDITIONAL:

- IEEE Student Member since 2002
- Member of Golden Key Honor Society
- Member of Eta Kappa Nu Honor Society
- Competed in CADathlon at ICCAD in 2004
- Demonstrated an Asynchronous Cellular Array Simulator at the ACM/SIGDA University Booth at DAC 2004