

EE100_Sp'09 Outcomes List – Kameshwar Poolla

By the end of the term, students having taken **Electronic Techniques for Engineering** are expected to:

- 1) Be able to analyze resistive circuits, transient analysis of single time-constant RC and RL circuits, simple op-amp circuits using the ideal op-amp method, and simple rectifier circuits.
- 2) Design and analyze combinatorial logic circuits, switching circuits such as registers, latches, and counters, and to conceptually understand how these are used to build larger systems such as memories.
- 3) Be familiar with fabrication techniques of integrated circuits, the operating principles of MOSFET transistors, understand the basic elements of communication systems such as modulation, multiplexing, and coding.