Students should:

* Be able to explain basic concepts of logic design: state, datapath + control, logic optimization, timing

* Understand sound digital-logic design methodologies

* Understand modern specification methods
  + Verilog

* Be able to use modern CAD tools to
  + Perform logic design
  + Validate designs via simulation
  + Map implementations into programmable logic devices

* Understand tradeoffs between hardware vs. software implementation.