Electronic Design Laboratory

TEXAS INSTRUMENTS
Electronic Design Lab “EE40” in Cory Hall

6.20.12

Scott McNally
Director of Space Planning
Department of Electrical Engineering and Computer Sciences

Shaffer Architects : Thad Shaffer

Cory Hall

EECS
Current conditions

- Under-utilized lab space
- Poor lighting
- Last update 1950
- Old equipment
- Electric breakers often trip
- No air conditioning
Cory Hall – Level 1

1st Floor - Plan

Existing TI Mixed Signals Systems Lab

Proposed Area of Work

EECS
Program Improvements

- Ideal visibility & access
- Improved circulation
- Display area
- Individual project area
- 22 workstations
- 1 TA area
- Air conditioning
- Power & networking
- Lab (1815 sq ft)
Scope of work

- Demo of display cases
- Slab opening
- New open stair to 1st floor
Scope of work

- Glass storefront for new lab
- Update finishes and lighting
Scope of work

- New finishes
- New lighting
- Demo of interior partitions
- New furniture
Scope of work

- New finishes
- New lighting
- Demo of interior partitions
- New furniture
Cory Hall – Level 1

Shaffer Architects

1. Partial first floor plan
2. Recessed monitors in main corridor
3. Monitor inside EE40 lab space
4. Recessed monitor at bottom of stair

Cory Hall Texas Instruments EE-40 Lab Remodel: Display Opportunities

EECS
### Cory Hall – Project Schedule

#### EECS

**Spring 2013**

- Program Confirmation
- Budget Confirmation with Chair
- Design, Contract Documents, Bid
  - Move Out
  - Demolition
  - UC CA Approvals
  - Bid Work
- Construction
- Opening

**Timeline**

- **May 24** (5/24)
- Spring 2013